

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Divisional application of S/N: 10/438,927:

Yasuro MATSUZAKI

Art Unit: 2824 (expected)

Serial No.: NEW

Examiner: V. Nguyen (expected)

Filed: April 23, 2004

Docket No.: 108066-00105

For: MEMORY CIRCUIT WITH AUTOMATIC PRECHARGE FUNCTION, AND
INTEGRATED CIRCUIT DEVICE WITH AUTOMATIC INTERNAL
COMMAND FUNCTION

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

April 23, 2004

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent Serial No. 10/438,927 filed on May 6, 2003 which is a divisional of U.S. Patent No. 6,636,449 issued on October 21, 2003.

The Commissioner is authorized to charge our Deposit Account No. 01-2300 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,



Wilburn L. Chesson
Attorney for Applicant
Registration No. 41,668

Customer No. 004372
AREN'T FOX, PLLC
1050 Connecticut Avenue, N.W., Suite 400
Washington, D.C. 20036-5339
Tel: (202) 857-6000
Fax: (202) 638-4810
RJH/kf

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 108066-00105	Serial No.: NEW
	Applicant: Yasurou MATSUZAKI	
	Filing Date: April 23, 2004	Group Art Unit: 2824 (expected)

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
	AA 6,137,743	Kim				
	AB 4,956,820	Hashimoto				
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	AH 61-71494	4/12/86	Japan	
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	AJ 11-298354	10/17/00	Japan	
	AK 58-48293	3/22/83	Japan	
	AL			

OTHER DOCUMENTS

AM		
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Examiner	Date Considered	